

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Kazunari KURITA et al.

Art Unit: not yet assigned

Application No.: not yet assigned

Examiner: not yet assigned

Filed: 04/19/2006

Attorney Dkt. No.: 12054-0059

For: PROCESS FOR PRODUCING HIGH-RESISTANCE SILICON WAFERS AND  
PROCESS FOR PRODUCING EPITAXIAL WAFERS AND SOI WAFERS (AS  
AMENDED)

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Prior to examination on the merits of the above-identified application, please  
amend the application as set forth below.

**Amendments to the specification are submitted on page 2.**

**Amendments to the claims are submitted on page 3.**

**Remarks are submitted on page 5.**